



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CHRT-99203.DIV

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Date of Deposit:	12/10/01	Name of Person Making the Deposit:	Julie Gibson	Signature of the Person Making the Deposit:

Inventor(s): Feng Chen, Lup San Leong and Charles Lin

Serial No.: 09/904,981

Group Art Unit:

Filed: 07/12/01

Examiner:

Title: POLISHING APPARATUS AND METHOD FOR FORMING AN INTEGRATED CIRCUIT

Assistant Commissioner for Patents  
Washington, D.C. 20231

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REQUEST FOR CORRECTED FILING RECEIPT

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- There is an error with respect to the following data, which is:  
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[] omitted.

Error In

- [X] Applicant's name
- [] Applicant's address
- [] Title
- [] Filing Date
- [] Serial Number
- [] Foreign/PCT Application Re:
- [] Other

Correct Data

- Feng Chen, Lup San Leong, and Charles Lin
- 
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3.

- A. ☒ The correction(s) is/are not due to any error by the Applicant and therefore no fee is due.

OR

- B. ☐ At least one of the above corrections is due to applicant's error and the fee therefor, under CFR 1.19(h), of \$25.00 is paid as follows:

☐ Enclosed is check for \$25.00.

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Please direct all correspondence concerning the above-identified application to the following address:

**WAGNER, MURABITO & HAO LLP**  
Two North Market Street, Third Floor  
San Jose, California 95113  
(408) 938-9060

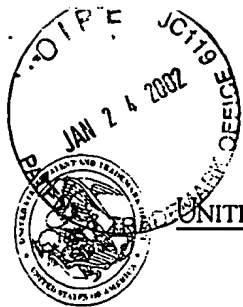
Respectfully submitted,

Date:

12/10/01

By:

John P. Wagner, Jr.  
Reg. No. 35,398



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/904,981 ✓	07/12/2001 -	2812	710	CHRT- 99203.DIV	12	9	1

CONFIRMATION NO. 3778

## FILING RECEIPT



\*OC00000006495147\*

WAGNER, MURABITO & HAO LLP  
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Date Mailed: 08/30/2001

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## Applicant(s)

Feng Gao, Singapore, SINGAPORE;

## Assignment For Published Patent Application

Chartered Semiconductor Manufacturing, Ltd.;

## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A DIV OF 09/415,364 10/08/1999  
AND A DIV OF 09/440,772 03/13/2000 \*

(\*) Data inconsistent with PTO records.

## Foreign Applications

If Required, Foreign Filing License Granted 08/28/2001

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

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Date: 9-5-01

Initials: P

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SEP 05 2001

WMH

Polishing apparatus and method for forming an integrated circuit

**Preliminary Class**

438

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**Data entry by : MA, WEI-WEN**

**Team : OIPE**

**Date: 08/30/2001**

**[REDACTED]**

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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